

Docket No.: 239708US2S

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

RE: Application Serial No.: 10/609,440

Applicants: Fumitaka ARAI, et al.

Filing Date: July 1, 2003

For: NON-VOLATILE SEMICONDUCTOR STORAGE

DEVICE COMPOSED OF NAND TYPE EEPROM AND DELETION VERIFICATION METHOD IN NON-VOLATILE SEMICONDUCTOR STORAGE

**DEVICE** 

Group Art Unit: 2818 Examiner: T. O. Le

SIR:

Attached hereto for filing are the following papers:

## LETTER SUBMITTING DRAWING REPLACEMENT SHEET(S) 2 REPLACEMENT SHEETS

Our check in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time.

Respectfully submitted,

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## IN THE UNITED STATES ATENT AND TRADEMARK OFFICE

IN RE APPLICATION:	Fumitaka ARAI, et al
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SEMICONDUCTOR STORAGE DEVICE

## LETTER SUBMITTING REPLACEMENT DRAWING SHEET(S)

COMMISSIONER FOR PATENTS Alexandria, VA 22313

SIR:

Responsive to the below indicated communication, the following drawing sheets are submitted herewith:

here	ewith:					
	2 Replacem	ent Drawing Sheets		_ New Drawing Sheets		
	Official Action dated					
	Notice of Allowance/Issue Fee dated August 12, 2004					
	Other	dated				
	The changes and/or modifications made include the following:					
	The legend "Prior Art" has been added to FIGS. 1, 2, and 3.					

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

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